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**Name:** Steve Bress

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**# Pages (incl. this):** 4

**Date:** 03/27/2007

**Message:**

Re: U.S. Application of : Steven Bress et al.

Application No.: 10/765,526

Filed: January 27, 2004

Title: Write Protection for Computer Long-Term Memory Devices With Multi-Port  
Selective Blocking

Attached is a Certificate of Correction and payment form 2038

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PTO/SB/44 (04-05)

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(Also Form PTO-1050)UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTIONPage 1 of 1

PATENT NO. :

APPLICATION NO.: 10/765,526

ISSUE DATE :

INVENTOR(S) : Steve Bress, Mark Menz

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Paragraph 12, "FIG. 10" should be "FIG. 1".

In Paragraph 13, "FIG. 11" should be "FIG. 2".

In Paragraph 13, "FIG. 10" should be "FIG. 1".

In Paragraph 19, "FIG. 10" should be "FIG. 1".

In Paragraph 21, "FIG. 11" should be "FIG. 2".

The abstract should be replaced by the following:

The current invention is a blocking device that provides read and write protection for computer long-term storage devices, such as hard drives. The blocking device is placed between a host computer and the storage device. The blocking device intercepts communications between the host and the storage device and examines commands from the host to the storage device. Certain commands, such as commands that may modify the storage device, may be discarded. Our current invention enables multiple host computers to communicate to the blocking device. The current invention selectively blocks only specified host computers.

Amended drawings are attached.

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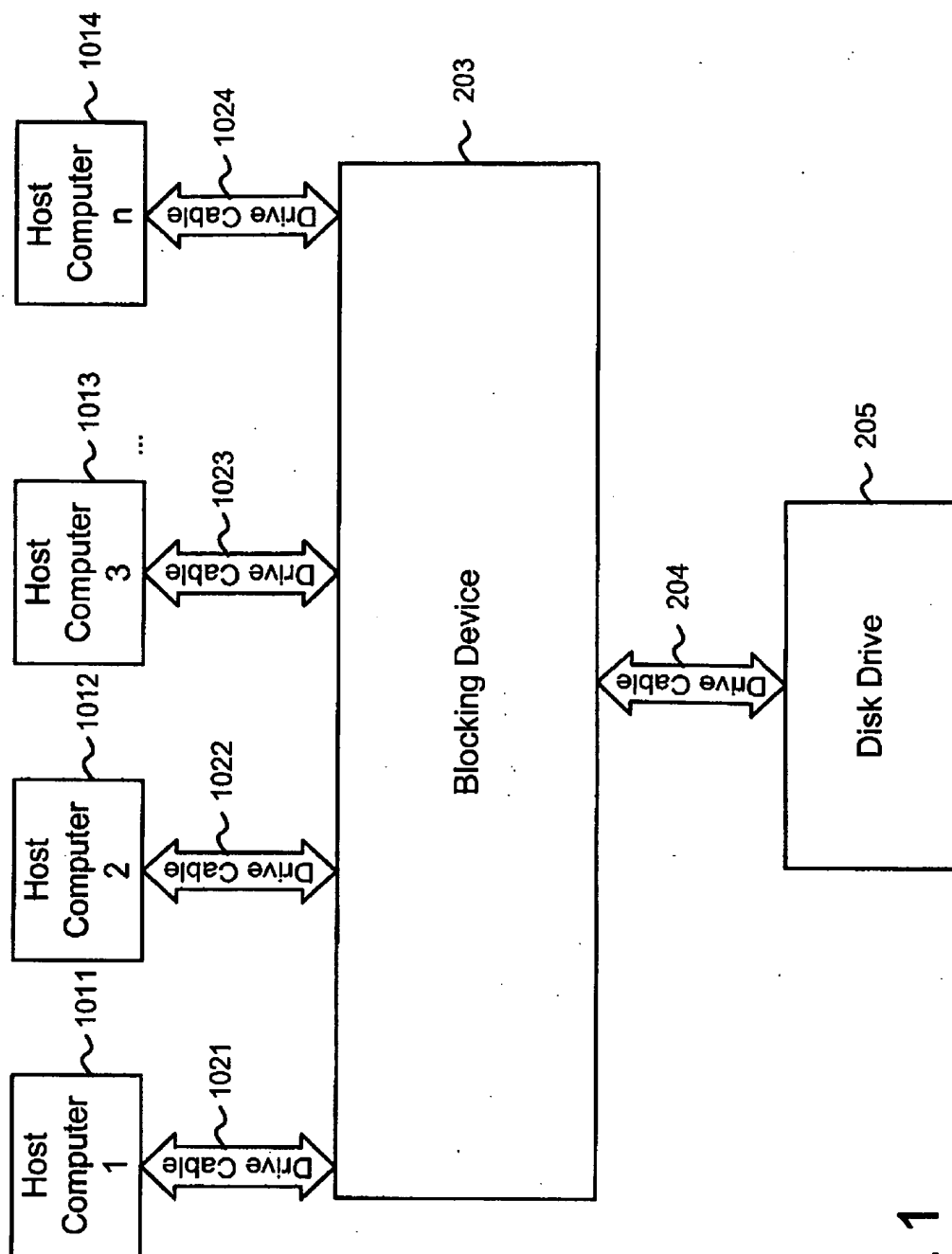


Fig. 1

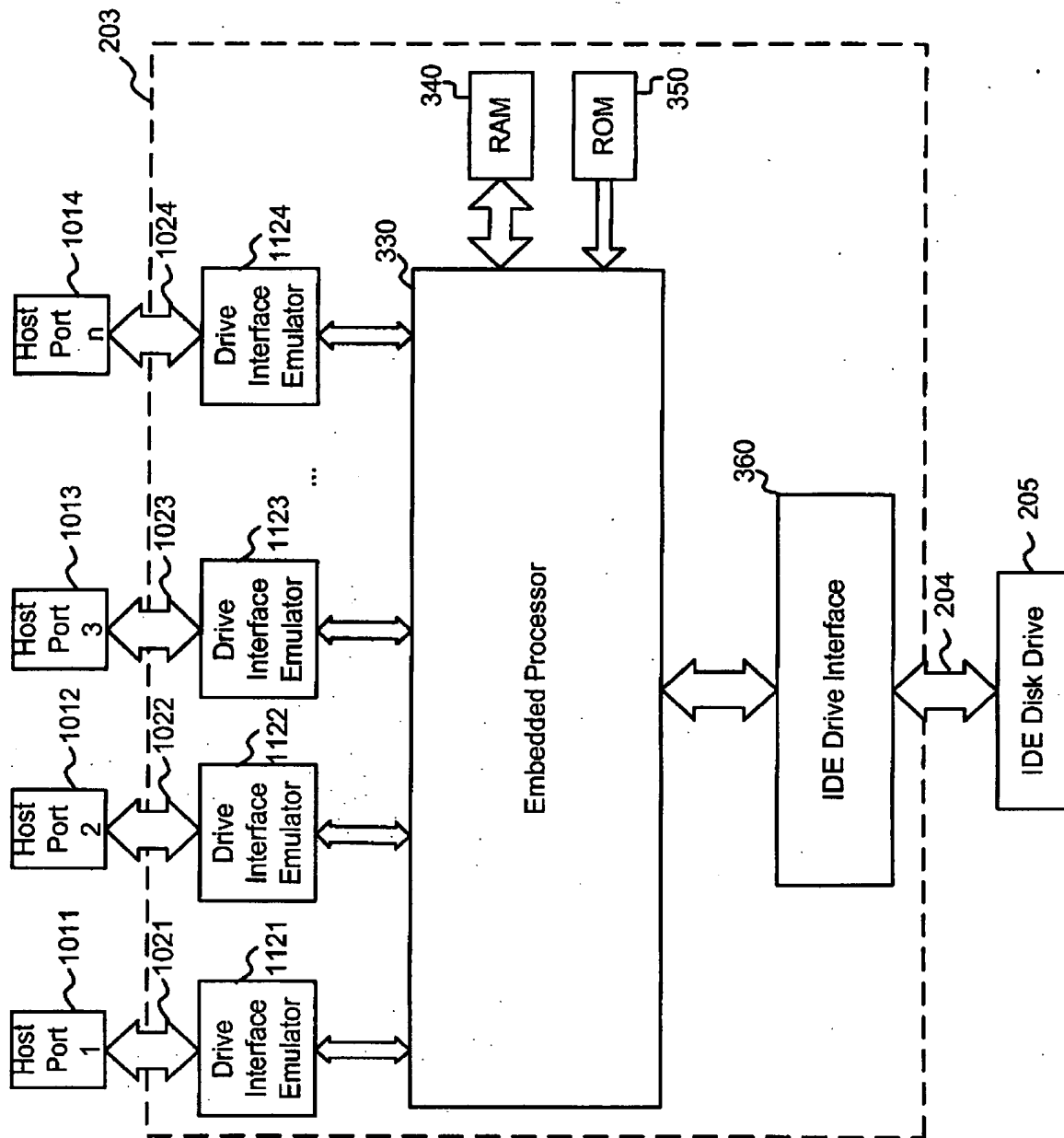


Fig. 2